

The one voltage state in a logic circuit. Though it may vary in actual positive voltage, it is considered a logical one. In TTL circuits, it is 2.4 volts and above. In CMOS designs, it is 3.2 volts and above. The supply voltage is assumed to be 5 volts.

From:

<http://www.midibox.org/dokuwiki/> - **MIDIbox**

Permanent link:

http://www.midibox.org/dokuwiki/doku.php?id=high_or_hi

Last update: **2006/10/15 09:35**

