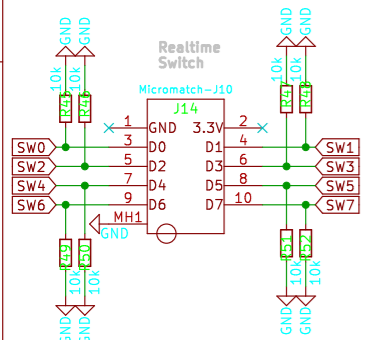
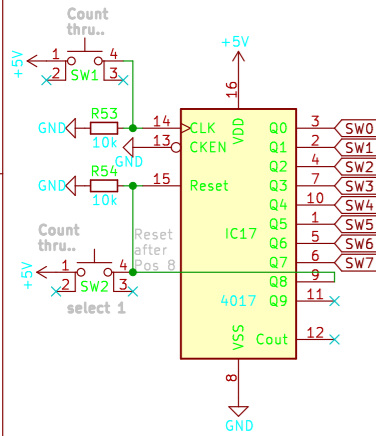


SWITCH BOARD

Johnson Decade Counter:

- By Press the Normally Open Push-Button you:
- * Activate the next Output
- * While all others gets deactivated
- * By reaching the last output, it restarts on the first out again.



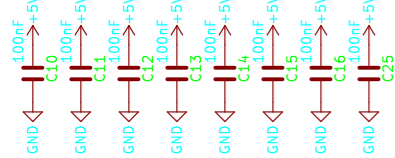
RealTime-Switch:

- Micromatch J10
- * Pin 0 goes to Core 1 J10-Pin0
- * Pin 1 goes to Core 2 J10-Pin0
- * Pin 2 goes to Core 3 J10-Pin0

- If a core receives a "HI" then:
- * It wait a few milliseconds and then
- * the Scan Routine is initialized (DIN)
- * the Display driver is initialized (expansions...)

- If a core receives a "LO":
- * constantly ignore all SRI events
- * mute all SRO Code

Bypass Caps
For each IC
To stabilize and Filter the Rails



Mounting Holes



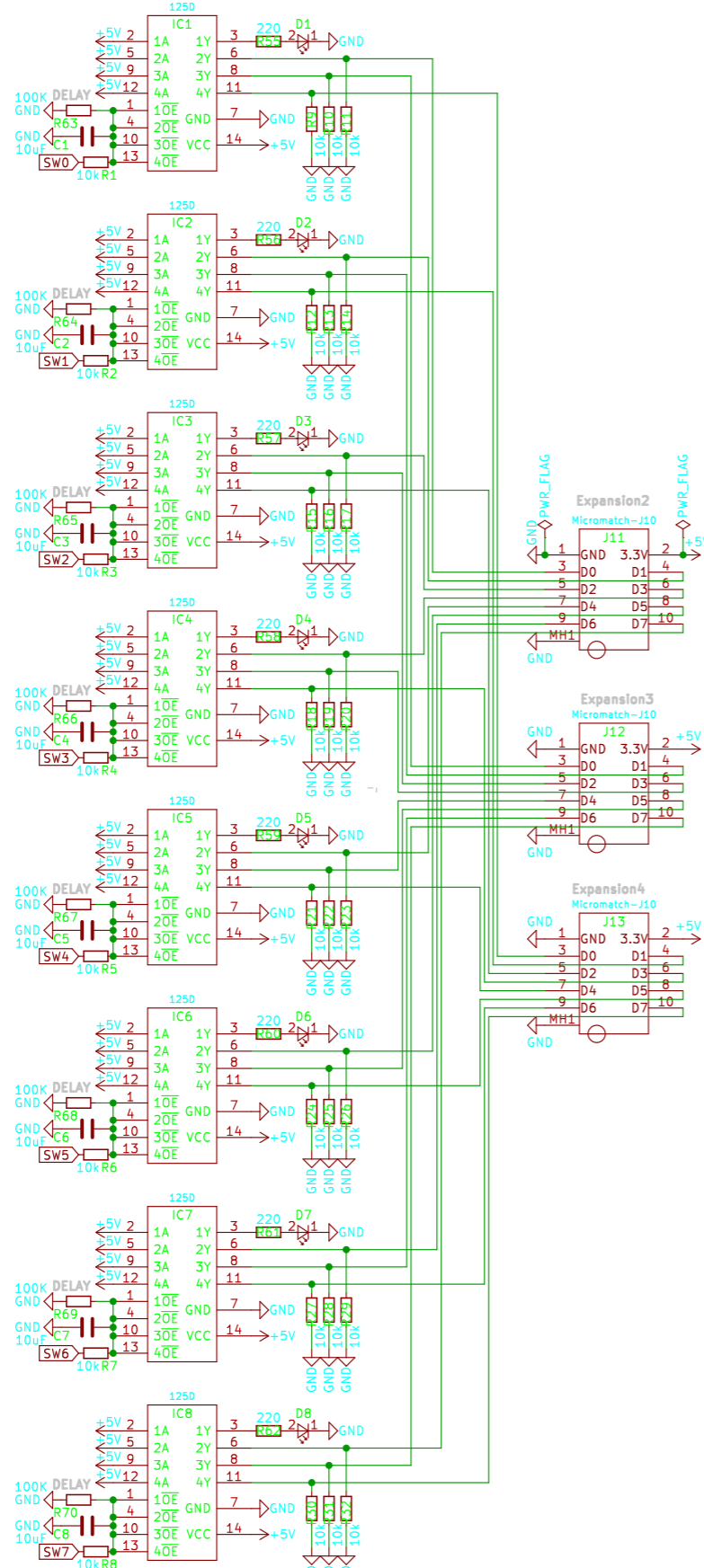
Switch Delay:

- This is needed, that nothing is done Hardwareside (switching Lines) until:
- * The Cores received a Lo/HI on J10 Pin0 - so they can stop Scan SRI or useless send out SRO
- * This STOP will need some cycles (Polling J10... do programm stuff)

- In the meantime the Capacitor is loadet to 2.5V or above which is a TTL HI:
- * This will forces the 74HC125 to switch the SRI0 Lines (J89 to several CORES)

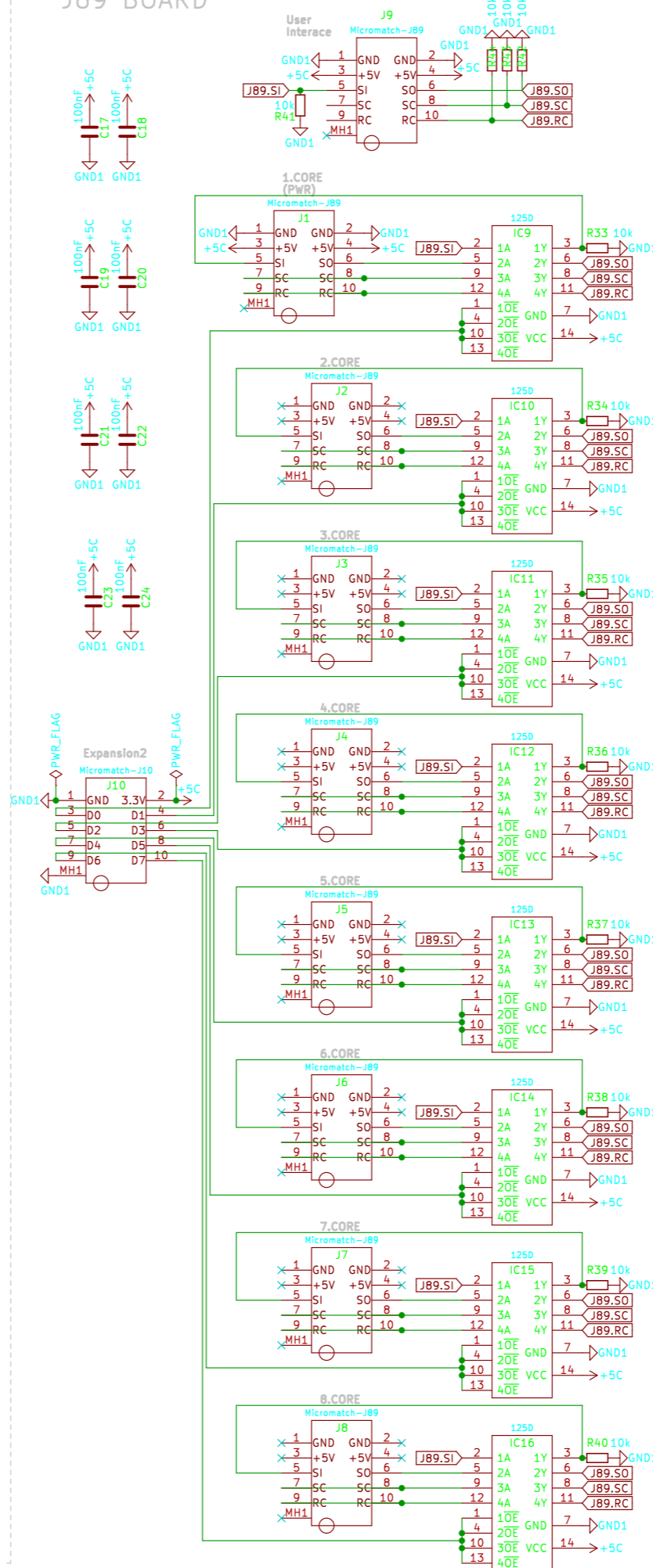
- Also in the meantime the other Capacitors are discharged - 2.5V or below:
- * This will forces all other 74HC125 to switch off their SRI0 Lines

The Discharge need to be quicker of course...



- ### 4In/OUT Buffer:
1. LEDs (onboard for Status)
 2. Expansion (SRI0 J89)
 3. Expansion (J15A,B Switch Screens)
 4. Expansion (same as SRI0)

J89 BOARD



3. Expansion:
This is for a extra Board where we have additional 74HC125 to Route for example:
* J15A, J15B ---- for LCDs

4. Expansion:
This is for a extra Board where we have additional 74HC125 to Route for example:
* J10B

ALL "125D" are SN74HC125D

Michael Sigl
www.siglcut.at

Sheet: /
File: SRI0_ROUTE.kicad_sch

Title: SRI0-Router

Size: A3 Date: 2022-01-07
KiCad E.D.A. kicad (6.0.0)

Rev: a
Id: 1/1