Gate that produces a logic 1 when all of its inputs are 1. In all other cases the output is 0.

From:
https://www.midibox.org/dokuwiki/ - **MIDIbox**

Permanent link:
https://www.midibox.org/dokuwiki/doku.php?id=and&rev=1160904901

Last update: **2007/11/17 17:53**